UNITED STATES PATENT AND TRADEMARK OFFICE **CERTIFICATE OF CORRECTION**

PATENT NO.

: 7,043,646 B2

Page 1 of 1

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INVENTOR(S)

: K. Enami et al.

It is certified that error appears in the above-identified patent and that said Letters Patent is hereby corrected as shown below:

<u>COLUMN</u>	<u>LINE</u>	ERROR
Item (56) Title page, col. 2	Refs. Cited (Other Publs., Item 1)	"Sirial Bus" should readSerial Bus
10 (Claim 15,	19-20 lines 4-5)	"means comprises a device controller means comprises a device controller" should read means comprises a device controller

Signed and Sealed this

Twenty-sixth Day of December, 2006

JON W. DUDAS Director of the United States Patent and Trademark Office